MEEN 433/667  Mechatronics

Basic principles of digital logic and analog circuits in mechanical systems; electrical-mechanical interfacing; sensors and actuators; digital control implementation; precision design and system integration. Three credit hours (2-3).

Instructor:

Won-jong Kim, Ph.D.
Associate Professor
223 Mechanical Engineering Office Building
(979)845-3645, phone
wjkim@tamu.edu, email
http://alum.mit.edu/www/wjkim, webpage

TA:

Mr. Jim McCabe
jimmccabe@tamu.edu, e-mail

Lectures:

MW 12:40 PM–1:30 PM at 206 ENPH

Labs:

F 12:40 PM–3:30 PM at 302 ENPH

Each lab consists of a 1-hour lab instruction followed by a 2-hour hands-on experiment. Lab attendance is mandatory.

Office Hours:

Dr. Kim: M 3:30 PM–4:30 PM and W 1:30 PM–2:30 PM at 223 MEOB or by appointment
Mr. McCabe: by appointment

Textbooks (required):


We will cover Chaps. 3, 5–7, and 9–11 of AH and Chap. 8 of FP in class. Chaps. 1–2, 4, and 8 of AH are assigned to read in the first week.
References:


Reading assignments will be given from these references. All references can be checked out for two hours from the Reserved Books Desk at Evans Library.

On-Line Course Materials:

A course web page is being established at eCampus.tamu.edu. You should be able to access the web page if you are registered for the course. All course-related material you will need, e.g. lecture slides, handouts, homework sets, solutions, lab descriptions, photographs of previous term projects, data sheets, manuals, and other useful materials will be placed.

Prerequisite: MEEN 364

- Dynamic systems and controls (MEEN 364)
- Sophomore-level electronics (ECEN 215)
- Hands-on instrumentation labs (MEEN 260)

Both ECEN 215 and MEEN 260 are prerequisite to MEEN 364. I expect you to be familiar with the following topics.

Dynamic systems and controls:
- Writing equations of motion and finding transfer function
- Determining system (time and frequency) responses
- Designing simple controllers (PID, etc.)
- Determining system stability

Sophomore-level electronics:
- Constitutive equations for passive elements, such as R, L, and C.
- Applying KVL and KCL for circuit analysis.
- Analyzing basic RLC circuits
- Analyzing basic OP Amp circuits

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Hands-on instrumentation labs:
- Using basic instruments, such as oscilloscopes, function generators, power supplies, etc.
- Designing and performing basic engineering experiments.
- Writing technically sound proposals and reports.

I also expect that you are fluent in any programming language, such as C, Matlab, C++, Java, Fortran, or any assembly language.

Course Objectives:
- Understand key contemporary issues in system integration with sensors, actuators, and real-time controllers.
- Cultivate confidence in your capability to design a microcontroller-based mechatronic system.

Course Learning Outcomes:
Upon successful completion of this course, you will be able to:
- Understand state-of-the-art microcontroller structures and their applications.
- Understand basic working principles of active electronic devices, such as BJT, FET, CMOS, and OP amp.
- Read and understand manufacturers’ data sheets.
- Design combinational and sequential digital logic circuits with off-the-shelf ICs.
- Build analog and digital interface.
- Develop working knowledge in key sensors and actuators and their applications.
- Design and implement simple digital controllers.

Grading:
- Mid-term exam for 28%
- Final exam for 35% (approximately 6% from lab materials)
- Homework for 10% total
- Term project for 27%
- Lab attendance (If you miss a lab without university-approved excuses, 3% of the total grade per absence will be deducted.)
- Lab safety (See the section titled, Laboratory Safety on page 5. Upon each lab-safety violation, 1% of the total grade will be deducted.)

Class participation (up to 5%) will not be quantified, but may affect the final grades for those on grade borderlines. This class participation includes participation in discussion in lectures and labs and during office hours of the instructor and the TA, volunteering to answer/solve problems, and asking intelligent questions to enhance the class’s understanding of the course material.

Exams:
- Mid-term exam: 12:40 PM–2:40 PM, likely on Friday, November 3, 2017 at TBA
- Final exam: 10:30 AM–12:30 PM, Monday, December 11, 2017 at 206 ENPH. Comprehensive. Covers all lecture and lab materials.

All exams are open-book, open-note. No electronic device except a calculator is allowed. In other words, you may bring any materials written or printed on paper media to the exams. However, any materials authored or collected by anyone else who took or is taking this course are strictly prohibited.
In addition, you may not share any materials with your classmates during the exams. You should expect substantial design problems (in hardware and software) that require creative application of the course materials.

Homework:

There will be 14 homework sets to be handed out every Monday. Each homework set is due the beginning of the class on its designated due dates. Homework sets may contain design problems (in software and hardware) to prepare you for the labs, exams and term project. Your homework must represent your own work. No late homework will be accepted.

Laboratory Safety:

Refer to the document titled, Laboratory Safety: Basic Student Guideline, posted at eCampus. You are asked to sign and return the safety contract to me by the first lab.

Term-Project Grading:

Term-project evaluations are scheduled on Friday, November 17, 2017 and during the term-project presentation on Monday (redefined Friday), December 4, 2017.

- Term-project proposal for 4% (clear idea and goal (2%), well-thought-out milestones (2%))
- Satisfactory progress by November 17, 2017 for 4%
- Successful use of course materials for 8% (analog electronics (2%), digital logic (2%), sensors (2%), actuators (2%)). You must demonstrate the full functionality of each item in your final project presentation to get the full credit.
- Presentation for 5% (quality of presentation, (2%), achieving the project goal (3%))
- Term-project report for 6% (lab note (3%), term-project report (3%))
- Bringing a novel idea not covered in the course for bonus up to 5%

How to Choose Term-Project Topics:

See the above term-project grading policy and design your project so that you can demonstrate your knowledge and capabilities. If you borrow someone else’s ideas in part, you must cite proper references. Otherwise, it will be considered plagiarism. All term-projects must employ a microcontroller development kit or equivalent.

Term-Project Expenses:

There will be a small fund available for your term project, and you can purchase materials and supplies up to your budget that I approve. There are two methods to use this fund. (1) You yourself make an arrangement with your vendor to send the bill directly to our department with MEEN 433/667 listed as a reference or PO Number. If your vendor does not honor this request, (2) you may check out the TAMU purchase card from 100A MEOB and use the MEEN 433/667 course account 239134-10160 for your purchase. This card must be returned the same day, and you cannot write down the card number for future use. In other words, you should check out the card each time you make a purchase. In case there is any incident of abuse, this privilege will be revoked. Note that TAMU will not reimburse any Texas sales tax you would pay. Some key parts may have a long lead time. You will be responsible for the consequence from any late delivery of parts.

The Mechanical Engineering Machine Shop located on the first floor of ENPH is available for your term project. You can use the machine tools in the machine shop free of charge for the term project if you do the labor yourself. A 3-D printer is also available free of charge for your term project.
Term-Project Proposal:

Each group brings an idea and submits a term-project proposal by the beginning of the lab on Friday, October 13, 2017. No late proposal will be accepted. The proposal must be type-written and may not exceed 5 single-spaced pages with 12-point fonts. Include sections representing (1) Objective and significance, (2) Design concept with representative diagrams, (3) Work plan with detailed milestones, (4) Budget and budget justification, and (5) Anticipated results.

Term-Project Presentations:

- Monday (redefined Friday), December 4, 2017 at 302 ENPH. An approximately 15-minute time slot will be assigned to each group by random drawing.

Term-Project Report:

Each group must hand in lab notes and a type-written term-project report before their presentation time. No late submission will be accepted. The report must not exceed 10 single-spaced pages with 12-point fonts. Include sections representing (1) Objective and significance, (2) Professionally done engineering drawings for final design, (3) Experimental results, (4) Discussion and conclusions, and (5) Contributions of individual group members. On the report cover page, put the percentage effort made by each lab member and include the signatures of all lab members. Term-project proposals and reports will not be returned, so ensure you make copies for yourself.

Policy on Grading Complaints:

If you feel a mistake was made in grading any material, please first contact the person doing the grading within a week after the graded paper is distributed. If you are not satisfied with the resolution of the matter then talk to me. After the one week discussion period, we will not review your exams or change grades. Make your complaint to me in writing and via e-mail. Be specific about your complaints. Please note I do not negotiate my partial-credit policy with students.

Tentative Grading Policy:

- A: greater than 80%
- B: greater than 60% and less than 80%
- C: greater than 40% and less than 60%
- D, F: less than 40%

Absences:

Work missed due to absences will only be excused for University-approved activities in accordance with TEXAS A&M UNIVERSITY STUDENT RULES (see http://student-rules.tamu.edu/rule7.htm). Specific arrangements for make-up work in such instances will be handled on a case-by-case basis. In accordance with recent changes to Rule 7, please be aware that in this class any “injury or illness that is too severe or contagious for the student to attend class” will require “a medical confirmation note from his or her medical provider” even if the absence is for less than 3 days (see 7.1.6.2 injury or illness less than three days).
To Earn Graduate Credit:

MEEN 433/667 is a stacked course. University policy FS.18.033 requires that more rigorous activities be provided for graduate students to ensure work at the graduate level. To earn the MEEN 667 credit, you should design and successfully implement a real-time digital feedback controller in your term project. Failure in demonstrating real-time digital feedback control will lead to 10% point deduction in the overall course grade.

Useful Websites:

- Textbook: http://www.engr.colostate.edu/mechatronics
- Arduino: https://www.arduino.cc
- Fairchild Semiconductor Corp.: http://www.fairchildsemi.com
- Texas Instruments, Inc.: http://www.ti.com
- ASCII Code Table: http://www.asciitable.com or http://www.jimprice.com/jim-asc.htm

ADA Statement:

The Americans with Disabilities Act (ADA) is a federal anti-discrimination statute that provides comprehensive civil rights protection for persons with disabilities. Among other things, this legislation requires that all students with disabilities be guaranteed a learning environment that provides for reasonable accommodation of their disabilities. If you believe you have a disability requiring an accommodation, please contact the Department of Student Life, Services for Students with Disabilities, in Room 126 of the Koldus Building or call 845-1637.

Academic Integrity:

Aggie Honor Code: “An Aggie does not lie, cheat, or steal, or tolerate those who do.”

Upon accepting admission to Texas A&M University, a student immediately assumes a commitment to uphold the Honor Code, to accept responsibility for learning and to follow the philosophy and rules of the Honor System. Students will be required to state their commitment on examinations, research papers, and other academic work. Ignorance of the rules does not exclude any member of the Texas A&M University community from the requirements or the processes of the Honor System. For additional information please visit: aggiehonor.tamu.edu

On all course work, assignments, and examinations at Texas A&M University, the following Honor Pledge shall be preprinted and signed by the student:

"On my honor, as an Aggie, I have neither given nor received unauthorized aid on this academic work."

_________________________
Signature of Student

Schedule:

On the next page is a tentative schedule. The pace will be adjusted as the semester progresses. The numbers in parentheses to the right of the topics indicate the corresponding chapters of the textbook. Reading assignments are given in addition to corresponding textbook reading.
<table>
<thead>
<tr>
<th>Wk</th>
<th>Monday</th>
<th>Wednesday</th>
<th>Friday (labs)</th>
</tr>
</thead>
</table>
| 1  | Course Overview (1)  
AH 1–2 | Semiconductor Electronics (3)  
semiconductor physics, band theory, 
doping, PN-junction diode, Zener 
diode, light-emitting diode (LED) | No lab  
AH 4; SS 3 |
| 2  | BJT (3)  
bipolar-junction transistor (BJT), 
BJT common-emitter operation  
HH 1–2.9; SS 4 | BJT Applications (3)  
BJT applications for switching, 
semiconductor manufacturing  
MS 1, 9 | µC Overview (7)  
microcontroller architecture, 
instruction set  
AH 7.1–7.4; MK 10–12;  
HH 11 |
| 3  | Digital Circuits (6)  
DL (diode logic), DTL, TTL  
(transistor-transistor logic) | Combinational Logic (6)  
number system, Boolean algebra, truth 
table, DeMorgan’s theorem, bubble 
pushing, half adder, full adder | C Programming  
C language, communication  
AH 7.7; KR |
| 4  | Combinational Logic (6)  
Karnaugh map, logic 
minimization, binary subtractor  
MK 1–5 | Combinational Logic (6)  
binary parallel adder, carry look-ahead, 
 comparator, parity generator/ checker | Digital I/O and A/D (8)  
I/O and data acquisition  
AH 8; HH 9.15–9.26;  
MK 13 |
| 5  | Combinational Logic (6)  
decoder/demultiplexer, 
multiplexer/data selector, encoder | Sequential Logic (6)  
timing chart, RS latch, D flip-flop (FF), 
JK FF, T FF, switch debounce circuits | Interrupts  
polling and interrupts  
HH 10 |
| 6  | Sequential Logic (6)  
master-slave FF, edge-triggered 
FF, FF with data lockout  
MK 6 | Sequential Logic (6)  
shift register, data converter/ 
transmitter, binary ripple counter  
MK 7; HH 8 | Encoder  
interface with polling and 
interrupts |
| 7  | Sequential Logic (6)  
synchronous counter, BCD  
(binary-coded-decimal) counter, 
counter applications | Field Effect Transistor (FET) (3)  
JFET, MOSFET (metal-oxide 
semiconductor FET), MOSFET switch, 
CMOS (complementary MOS), SS 5 | Logic Gates and FFs (6)  
RS latch, D FF, JK FF, binary counter  
AH 7.6 |
| 8  | Large-Scale IC (6)  
CMOS logic and memory, flash 
memory, TTL-CMOS interfacing  
HH 3, 9.1–9.14; MK 9, 14 | Position Sensors (9)  
capacitance probe, LVDT, laser 
distance sensor, laser interferometer, 
optical encoder | 555 Timer and OP Amp  
monostable and astable 
oscillators  
HH 5.12–5.20 |
| 9  | Specialty Sensors (9)  
strain gauge, accelerometer, 
thermocouple, load cell, pyrometer  
S 3–4 | Electromechanical Actuators (10)  
voice-coil actuator, DC motor, PM 
motor, stepper motor  
WM 4; BV 1, 3, 4–5; C 4–7 | Sensors and Actuators  
interface with temperature 
sensor, IR (infrared) range 
sensor, and solenoid 
valve/relay |
| 10 | Smart-Material Actuators (10)  
piezoelectric, magnetostrictive,  
ionic-polymer actuators | Analog Signal Processing (5)  
voltage regulator, OP amps, differential 
amplifier, instrumentation amplifier, 
anti-aliasing filter, active filter  
HH 2.15–2.25, 4, 5.1–5.11, 6–7 | Mid-Term Exam  
12:40 PM–2:40 PM at TBA |
| 11 | Analog Signal Processing (5)  
low-frequency amplifiers, pulse- 
width modulation (PWM)  
amplifier, ground-loop elimination  
AH 7.8–7.9; HH 15 | Discrete-Time Systems (FP 8)  
difference equation, Z-Transform | PWM  
PWM, DC motor driving  
AH 10.5 |
| 12 | Digital Control (FP 8)  
design by emulation, digital PID  
and lead-lag compensators | Digital Control (FP 8)  
root locus, zero-order-holder (ZOH)  
equivalence | DC Motor Control  
digital controller design and 
implementation |
| 13 | Real-Time Control (FP 8)  
real-time control implementation | No lecture, Thanksgiving | No lab, Thanksgiving |
| 14 | Mechatronic Systems (11)  
precision design, interfacing,  
sensor mounting.  S 1–2 | Mechatronic Systems (11)  
control architecture, system integration.  
S 5, 7 | Term-Project Prep.  
HH 12 |
MEEN 433/667  Mechatronics
Fall 2017
Instructor: Prof. Won-jong Kim

Questionnaire

Name:
Email address:
Phone number:
TAMU ID:
TAMU Department:
Are you a senior, a master’s student, or a doctoral student?
Advisor (if applicable):

Have you taken ECEN 215 (or equivalent)?  Yes.  No.
Have you taken MEEN 260 (or equivalent)?  Yes.  No.
Have you taken MEEN 364 (or equivalent)?  Yes.  No.

What would you like to learn from this course?

Will the course materials be useful for your research? If so, please tell me about your research briefly.